5-06-041

2125/ #

HEWLETT-PACKARD COMPANY

Intellectual Property Administration P.O. Box 272400 Fort Collins, Colorado 80527-2400

Docket No.: 10007479-1

(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Richard A. Ferreri et al.

Application No.: 09/709,695

Art Unit: 2125

Filed: November 10, 2000

Examiner: A. Paladini

For: MEMORY EFFICIENT OCCURRENCE

MODEL DESIGN FOR VLSI CAD

RECEIVED

MAY 1 1 2004

Technology Center 2100

MS Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

INTRODUCTORY COMMENTS

AMENDMENT

In response to the Office Action dated February 23, 2004, the Examiner is requested to reconsider the application in view of the amendments and remarks below.

Amendments to the Drawings are described on page 2 of this paper.

Amendments to the Specification begins on page 3 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 4 of this paper.

Remarks begin on page 8 of this paper.

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